Final Exam

COMP - 520, Spring 2024

Please fill in today's date (YYYY-MM-DD)

Write your name and PID here:

Write your ALIAS here:

Your alias and PID should be written on ALL exam pages.

Your alias represents you and will be equivalent to your name throughout the exam.

Instructions

- 1. Open notes. (Anything you wrote down)
- 2. Open course website. (Including lectures)
- 3. Limited IDE access. (IntelliJ, Eclipse, Notepad, etc.)
 - a. IDE access is given so that you can reliably see the syntax highlighting of your code, and that's it!
 - b. You may open your IDE but not write any new code.
 - c. You may NOT compile any code. (javac)
 - d. You may NOT run anything in your IDE.
- 4. No websites other than the course website/gradescope.
 - a. No Piazza, Canvas, Sakai, email, etc.
 - b. You **MAY** use the web resources specified in PA4.
- 5. No phones at all, even if you forgot your laptop.
- 6. No collaboration. No communication with anyone other than instructors, TAs, and proctors.
- 7. Be concise in your answers.
- 8. You get the full exam duration (180 minutes).
- 9. Sign below with either your name or alias.

I pledge I have not given nor received unauthorized aid on this	s exam:
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PLEASE READ THIS PAGE ON GRADING

You only need 100 points to get a 100% on this exam.

Note: there are 5 questions each worth 25 points.

This means 125 points are possible.

Any excess of 100 points will be applied as extra credit with the weight of a midterm.

Cheat Sheet

Dataflow Analysis:

```
for(v \in V) {
   out(v) := \bigcup s: s \in successor(v) :: in(s)
   in(v) := use(v) \cup (out(v) \setminus def(v))
}
```

Invariants:

```
\begin{array}{l} \mathsf{I0} \equiv \ \mathsf{use}(v) \subseteq \mathsf{in}(v) \\ \mathsf{I1} \equiv \ \mathsf{out}(v) \setminus \mathsf{def}(v) \subseteq \mathsf{in}(v) \\ \mathsf{I2} \equiv \ \forall \ s : s \in \mathsf{successor}(v) :: \mathsf{in}(s) \subseteq \mathsf{out}(v) \end{array}
```

Notation:

Followers(A) \equiv FL(A) Starters(A) \equiv ST(A) Nullable(A) \equiv N(A)

Capitals are non-terminals, lowercase are terminals. Greek letters are sequences.

Nullable Induction:

Observed	Rule
1. $\alpha = \varepsilon$	Nullable(α) = true
$2. \alpha = t$	Nullable(α) = false
3. $\alpha = A$	$Nullable(\alpha) = Nullable(A)$
$4. \alpha = \alpha_1 \alpha_2 \dots \alpha_n$	$Nullable(\alpha) = Nullable(\alpha_1) \land \land Nullable(\alpha_n)$
5. $\alpha = \alpha_1 \alpha_2 \dots \alpha_n $	$Nullable(\alpha) = Nullable(\alpha_1) \lor \lor Nullable(\alpha_n)$
6. $\alpha = \beta^*$	Nullable(α) = true

Starters Induction:

Observed	Rule
1. $\alpha = \varepsilon$	$Starters(\alpha) = \{ \epsilon \}$
$2. \alpha = t$	$Starters(\alpha) = \{ t \}$
3. $\alpha = A$	Starters(α) = Starters(A)
$4. \alpha = \alpha_1 \alpha_2 \dots \alpha_n$	Starters(α) = Starters(α_1) \oplus Starters($\alpha_2 \dots \alpha_n$)
5. $\alpha = \alpha_1 \alpha_2 \dots \alpha_n $	Starters(α) = Starters(α_1) \cup \cup Starters(α_n)
6. $\alpha = \beta^*$	$Starters(\alpha) = Starters(\beta) \cup \{ \varepsilon \}$
$A \oplus B$	$B = \begin{cases} A & \text{if } \varepsilon \notin A \\ (A \setminus \{\varepsilon\}) \cup B & \text{otherwise} \end{cases}$

Followers First Step:

$$FL_0(A) = \left(\bigcup_{C ::= \alpha A \beta} ST(\beta)\right) \setminus \{\varepsilon\}$$

Followers Inductive Step:

$$FL_{i+1}(A) = FL_i(A) \cup \bigcup_{C := \alpha A \beta \text{ and Nullable}(\beta)} FL_i(C)$$

Followers Final Step:

$$\mathrm{FL}(\mathsf{A}) = \mathrm{FL}_n(\mathsf{A}) \cup \begin{cases} \{\varepsilon\} & \text{if } \mathsf{S} \Rightarrow^* \alpha \mathsf{A} \\ \{\} & \text{otherwise} \end{cases}$$

For each choice in A ::= $\beta(\alpha_1 | ... | a_m)\gamma$ define:

 $Predict(\alpha_i) = Starters(\alpha_i \gamma) \oplus Followers(A)$

For repetitions in A ::= $\beta(\alpha)^* \gamma$

 $Predict(\alpha) = Starters(\alpha)$

 $Predict(\gamma) = Starters(\gamma) \oplus Followers(A)$

For sequences A ::= $\alpha\beta\gamma$

 $\mathsf{Predict}(\mathsf{A}) = \mathsf{ST}(\alpha) \oplus \mathsf{ST}(\beta) \oplus \mathsf{ST}(\gamma)$

x64:

pop rm: Stores data at [rsp] into rm, and rsp += 8

push rm: Does rsp -= 8, then stores rm at [rsp]

mov rm,r: Takes r, stores it in rm

mov rm,imm32: Stores imm32 in rm

RM examples:

Form Example

r # rdx

[rdisp + disp] # [rbp-10]

[ridx*mult + disp] # [rcx*8+11223344]

[rdisp + ridx*mult + disp] # [rbx+rsi*4-8]

[disp] # [11223344]

Useful Registers:

rax, rcx, rdx, rbx, rsp, rbp, rsi, rdi r8, r9, r10, r11, r12, r13, r14, r15